

Fig. 1(c)

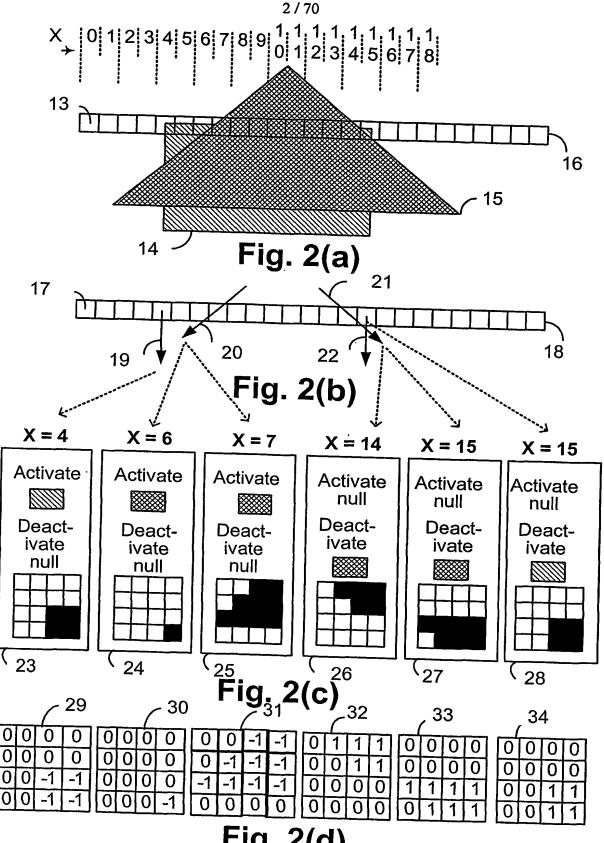
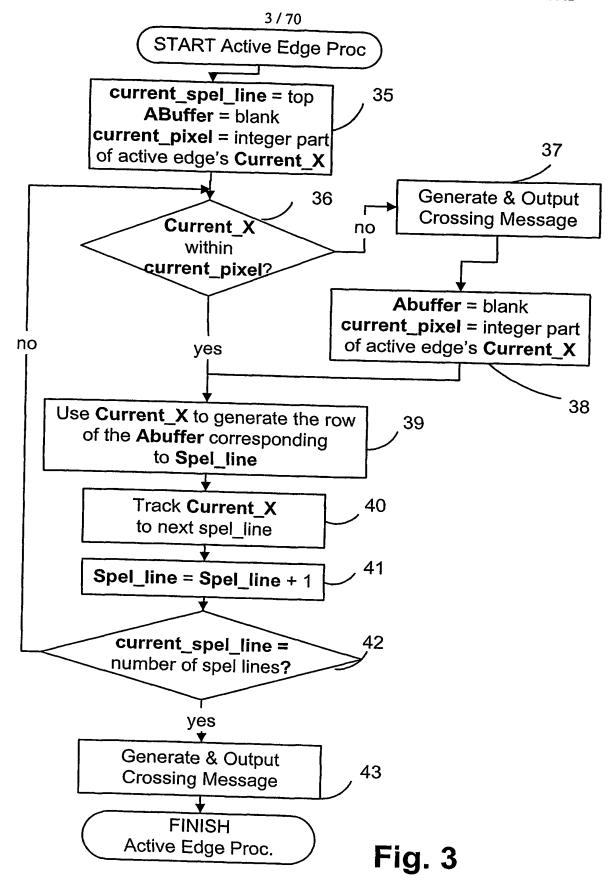
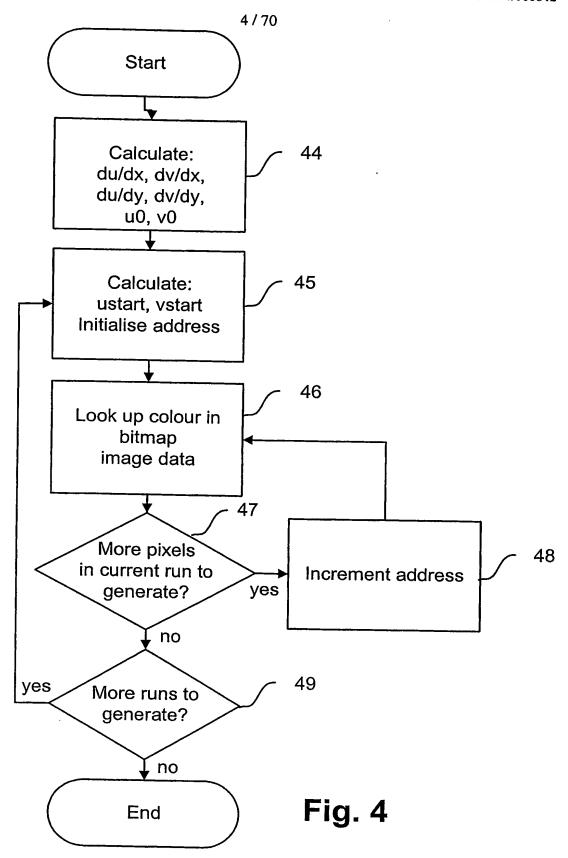


Fig. 2(d)





```
5 / 70
```

```
void
DrawLine
(
    int Xs,
    int Ys,
    int Xe,
    int Ye
)
{
            tx = Xe - Xs;
    int
    int
            ty = Ye - Ys;
    int
            grad = tx / ty;
    int
            ient = tx % ty;
            err = (2 * ient) - ty;
    int
            delta err1 = 2 * ient;
    int
            delta_err2 = 2 * (ient - ty);
    int
    int
            x = Xs;
    int
           y;
    for (y = Ys; y \le Ye; y++)
        SetPixel(x, y);
        if (err < 0)
             err += delta err1;
            x += grad;
        else
             err += delta err2;
            x += grad + \overline{1};
    }
}
```

Fig. 5

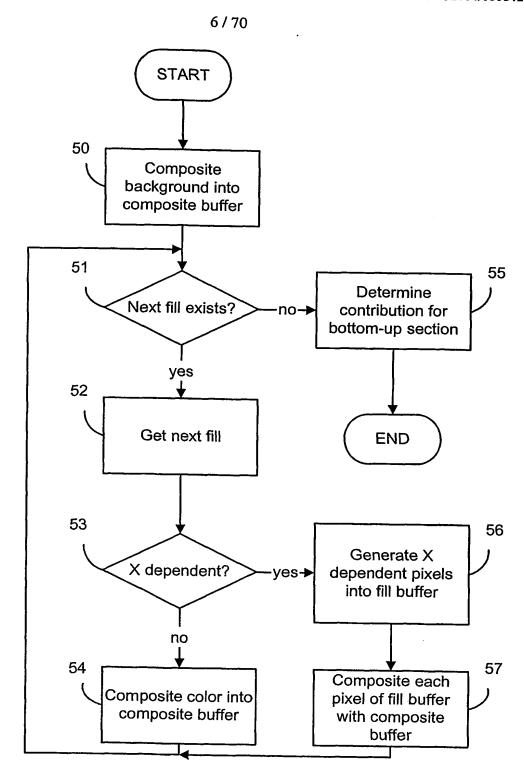


Fig. 6

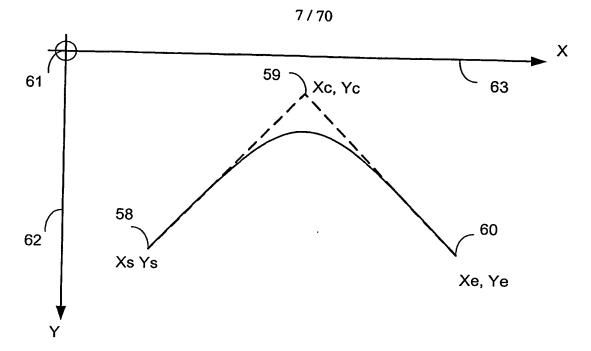
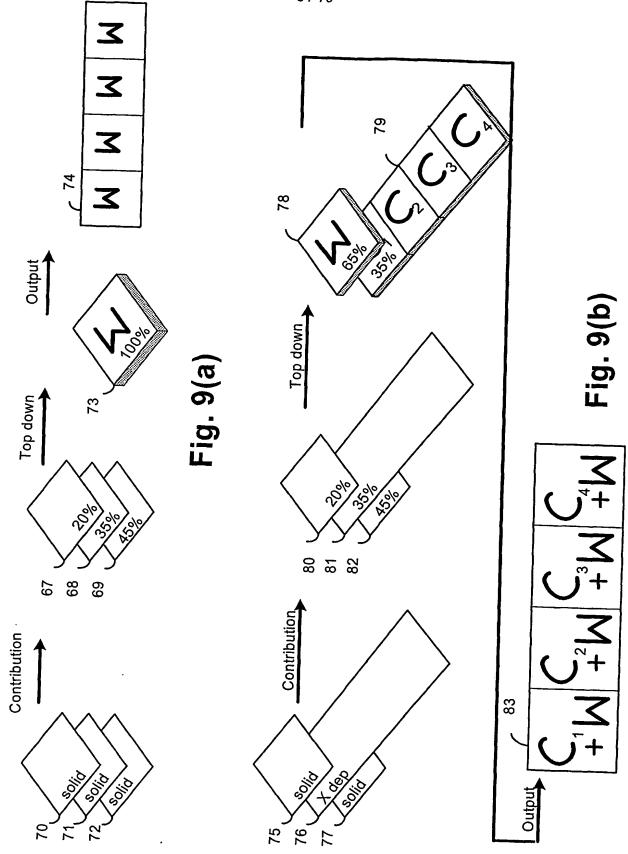
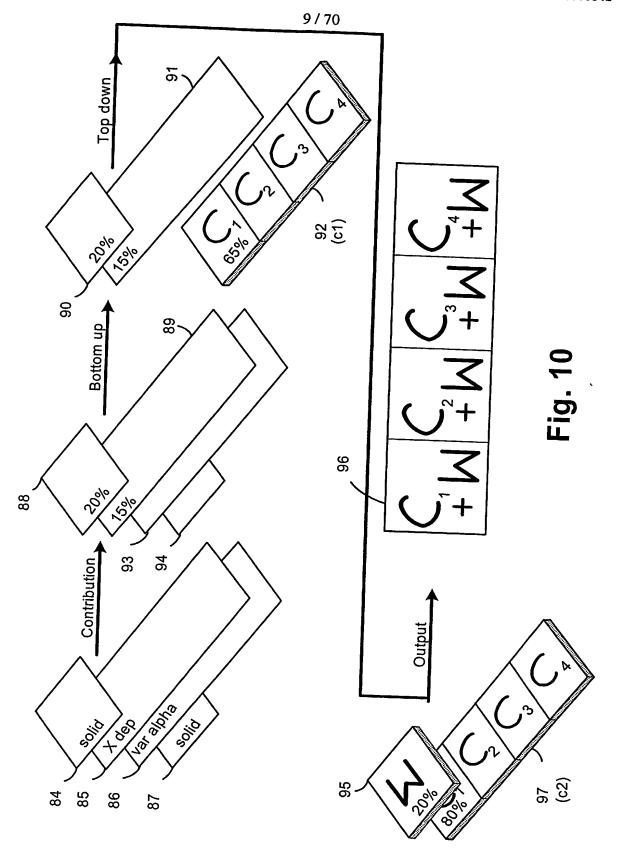
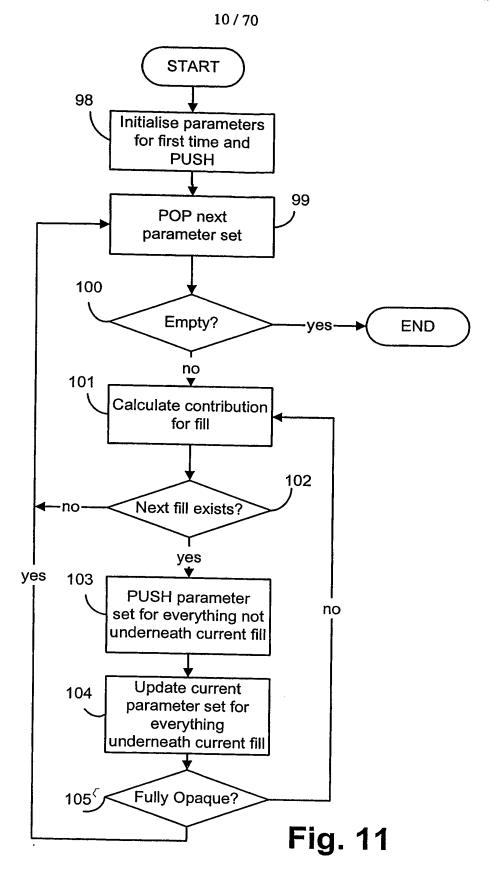


Fig. 7 64 X = 4**Activates** Z-level: 65 66 0 0 De-activates 0 0 0 0 0 Z-level: n/a Generate 0 0 0 0 0 0 Copy winding 0 winding 0 counts across counts -1

Fig. 8







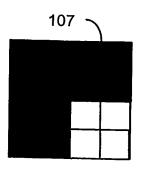
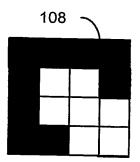
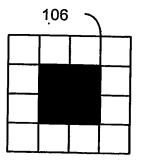


Fig. 12(a)



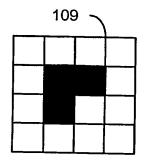
coverage \cap ~(coverage \cap A-buffer)

Fig. 12(c)



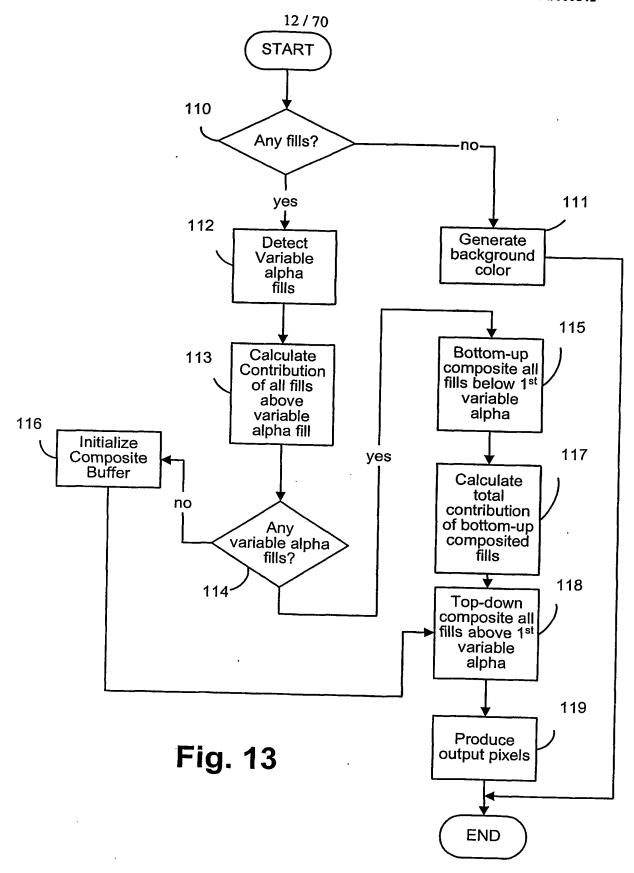
A-buffer

Fig. 12(b)



coverage ∩ A-buffer

Fig. 12(d)



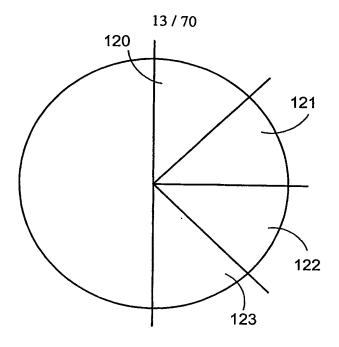
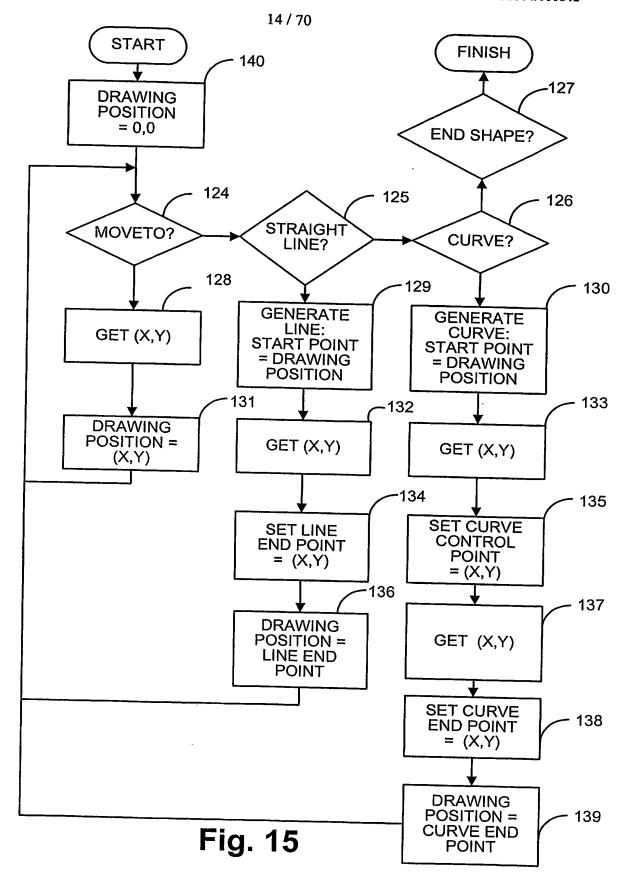
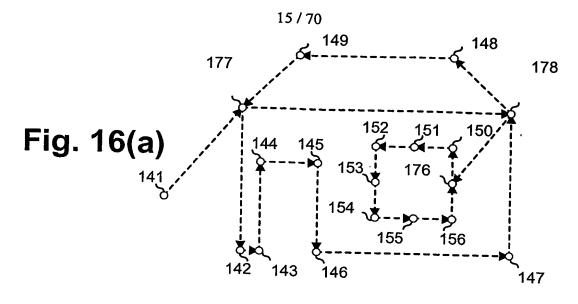


Fig. 14





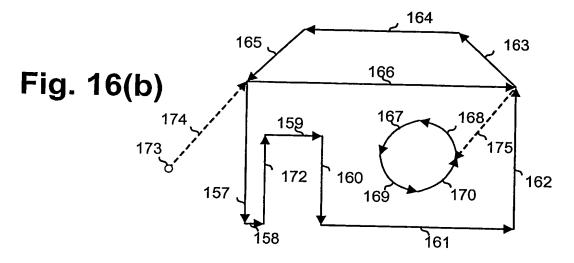
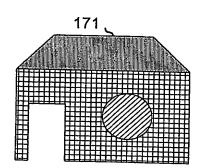


Fig. 16(c)





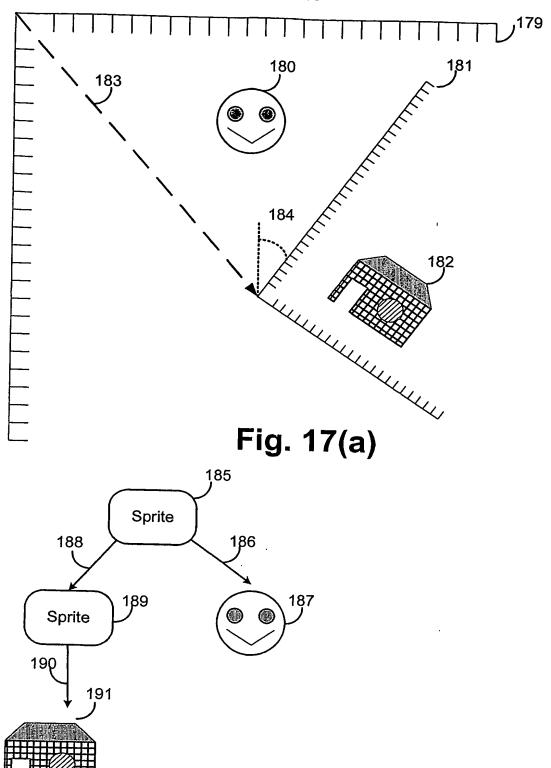


Fig. 17(b)

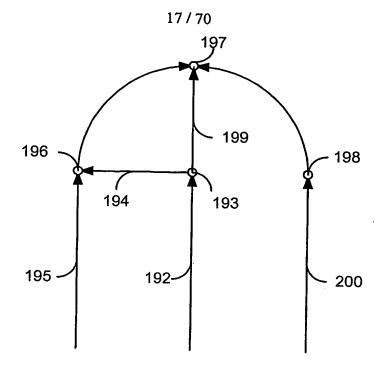


Fig. 18(a)

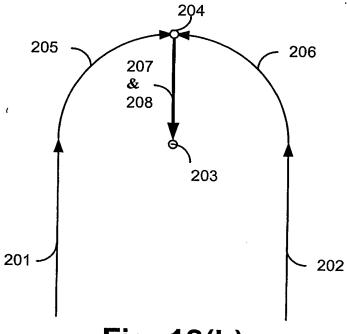


Fig. 18(b)

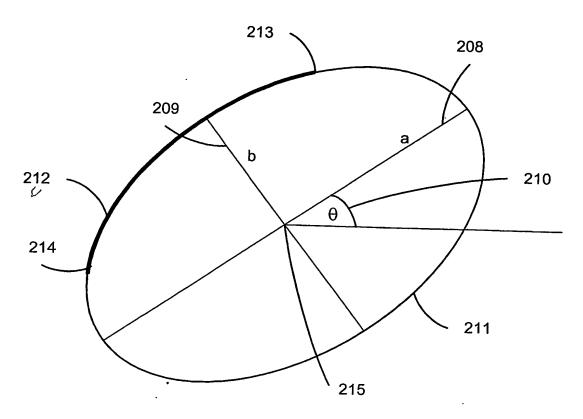


Fig. 19

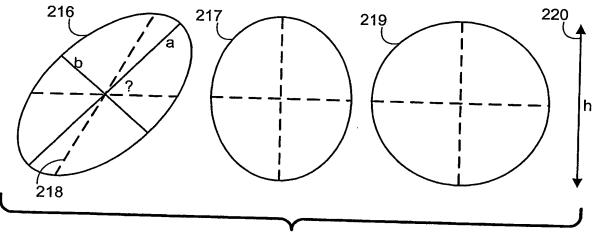
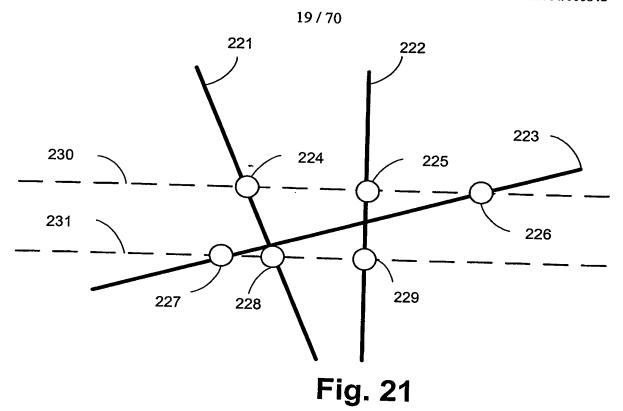


Fig. 20



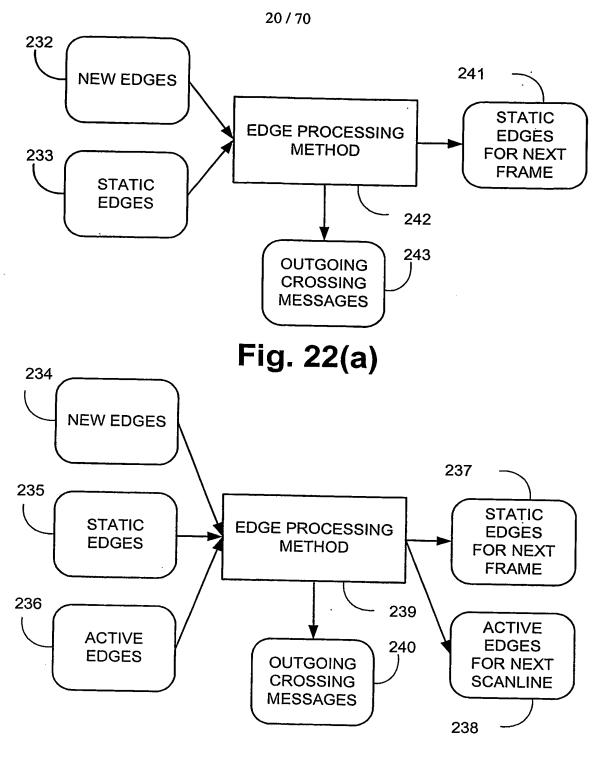
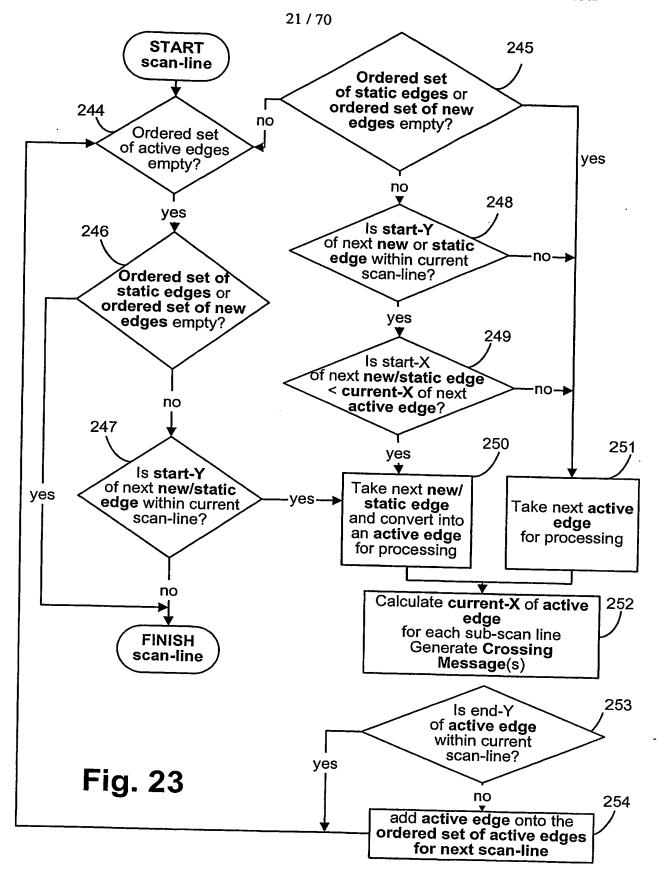


Fig. 22(b)



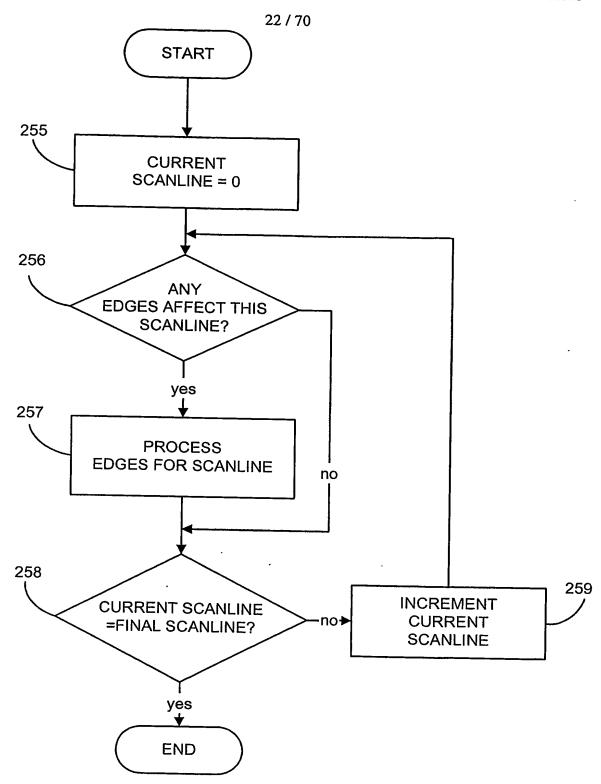


Fig. 24

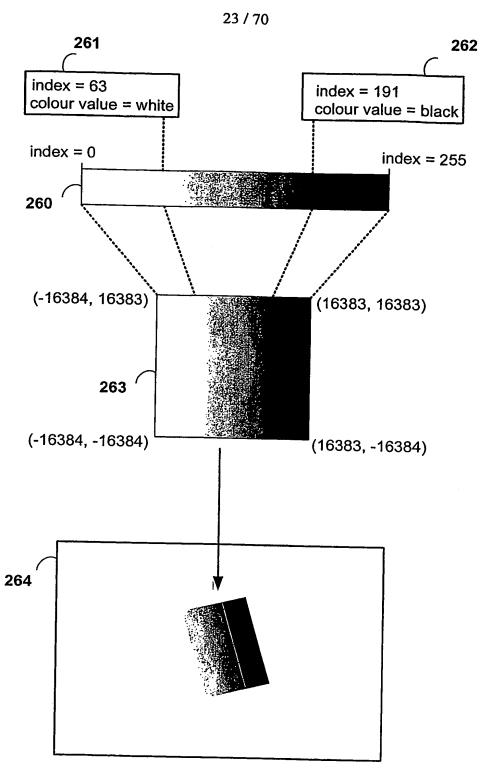


Fig. 25

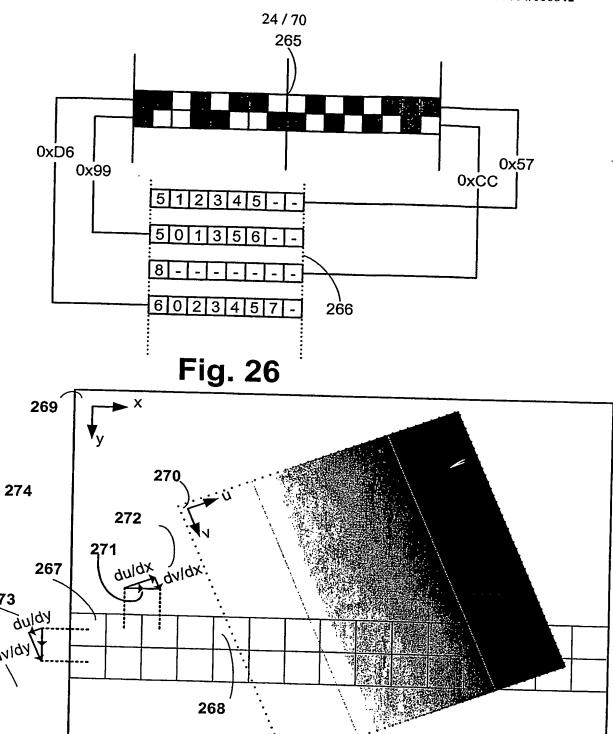


Fig. 27

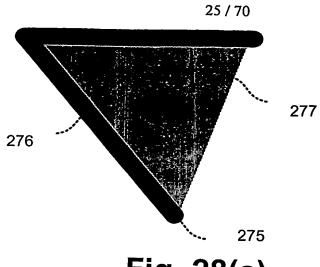


Fig. 28(a)

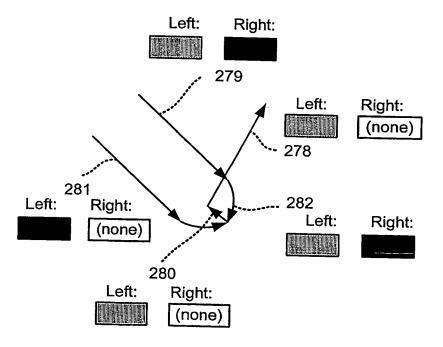


Fig. 28(b)

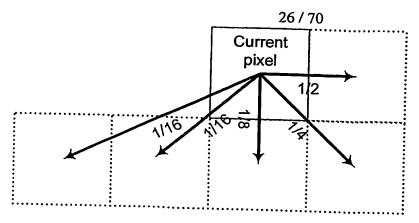


Fig. 29(a)

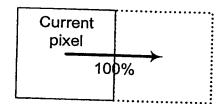


Fig. 29(b)

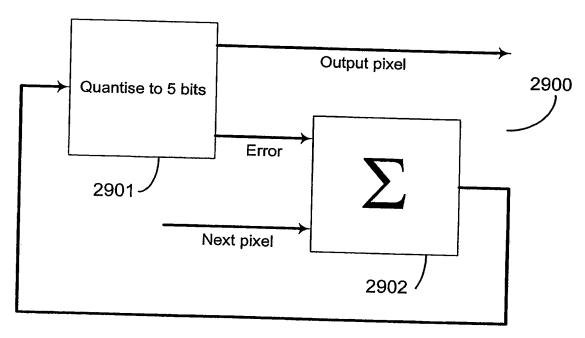
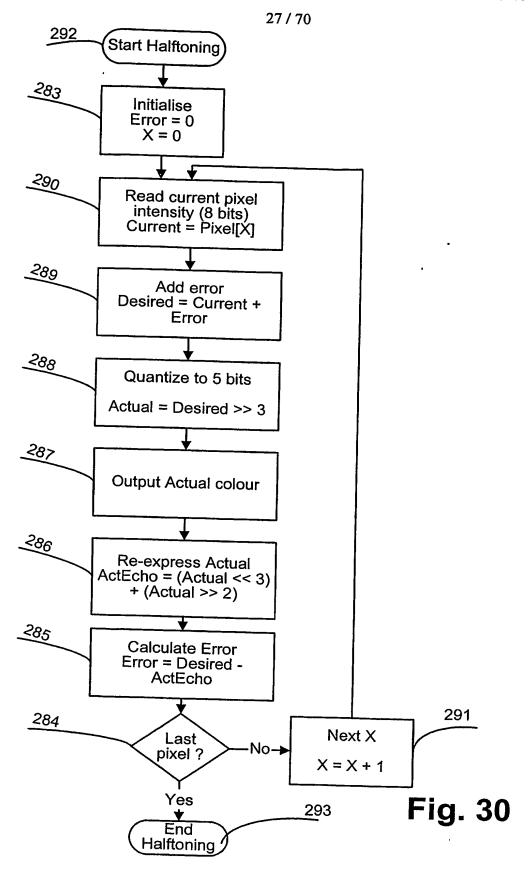
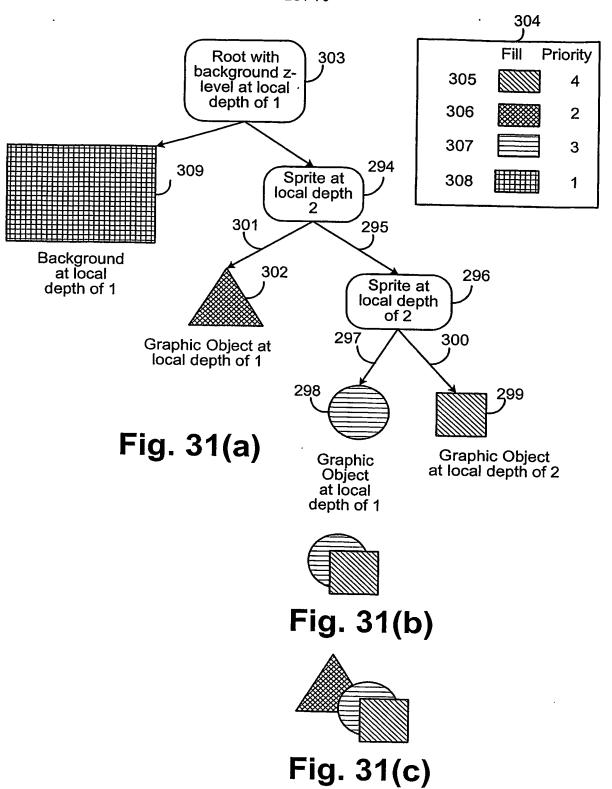
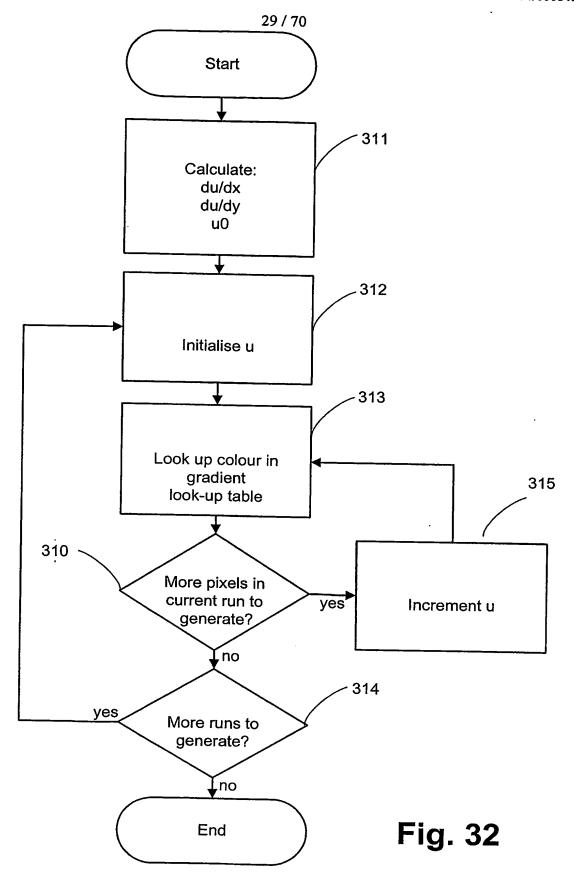
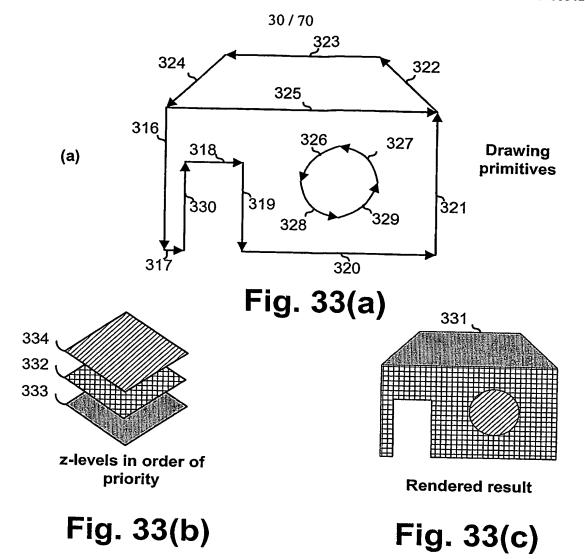


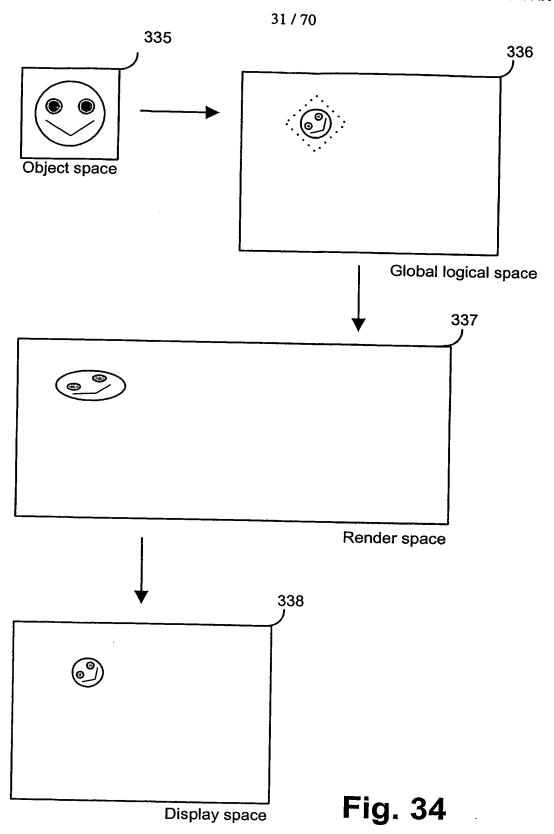
Fig. 29(c)











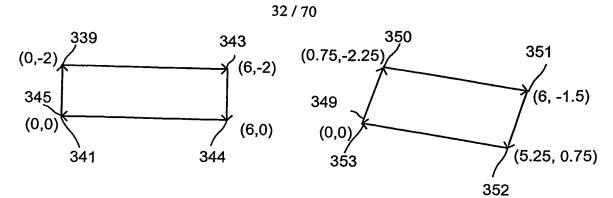


Fig. 35(a)

Fig. 35(b)

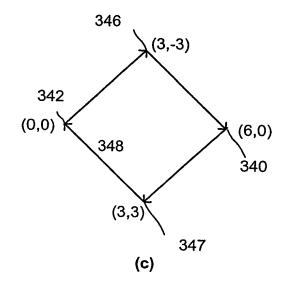


Fig. 35(c)

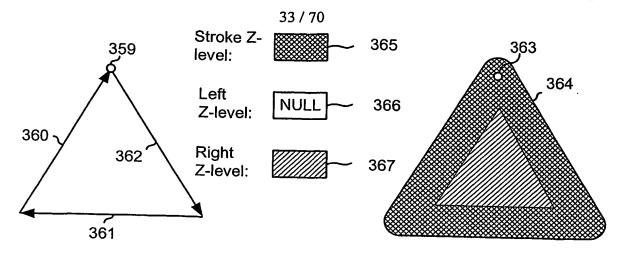


Fig. 36(a)

Fig. 36(b)

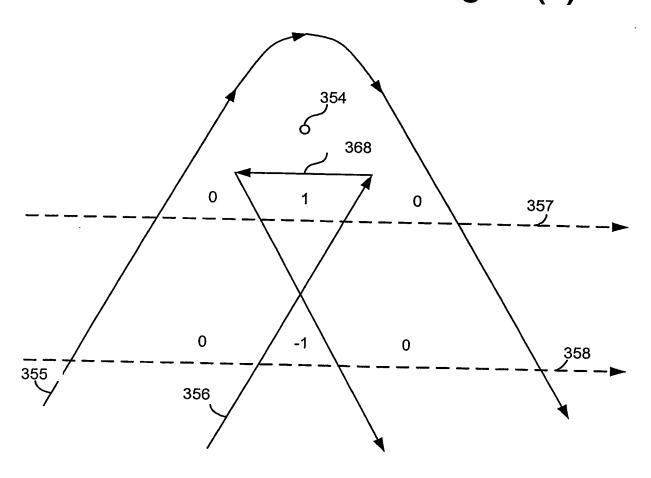
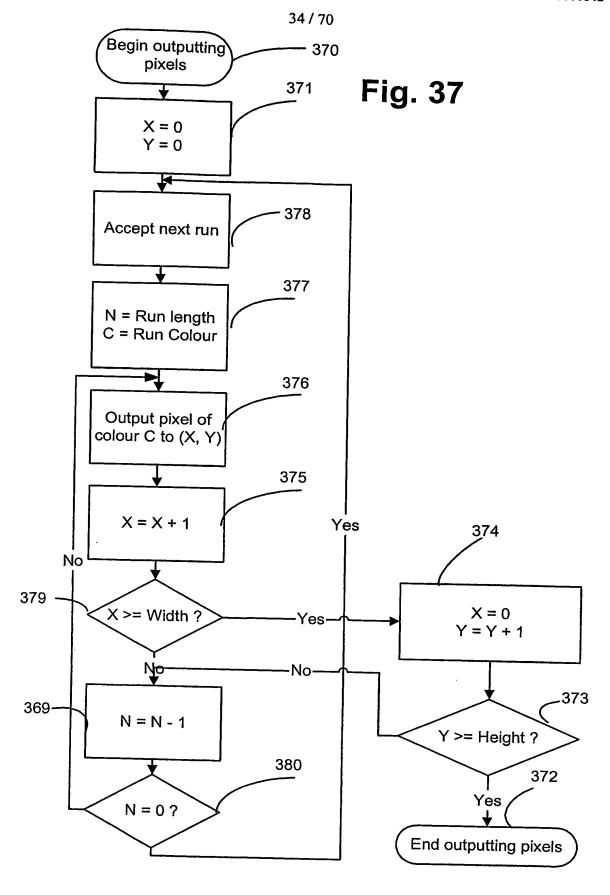
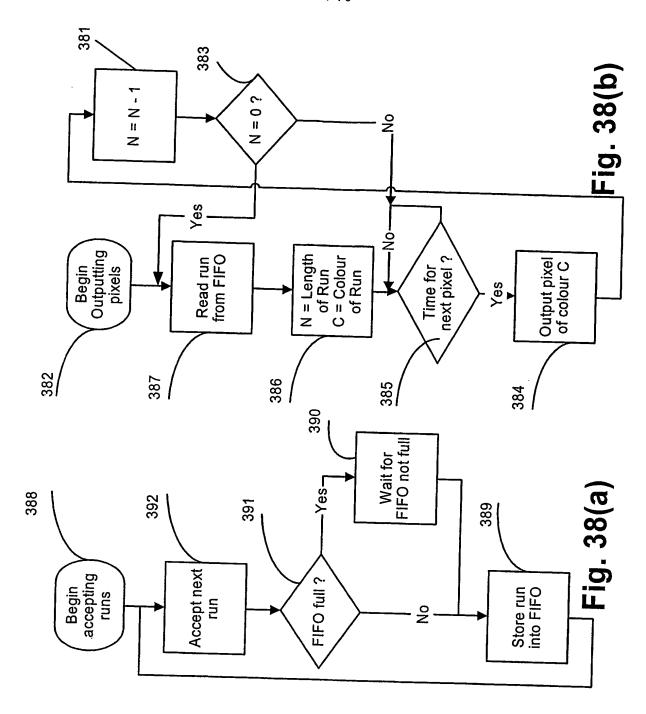


Fig. 36(c)





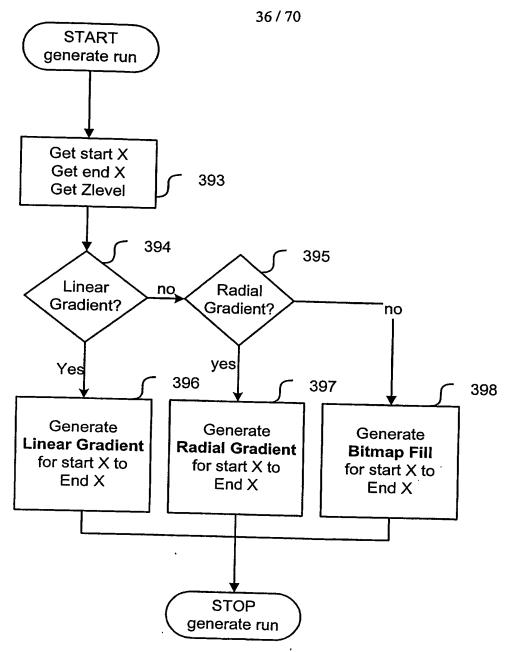


Fig. 39

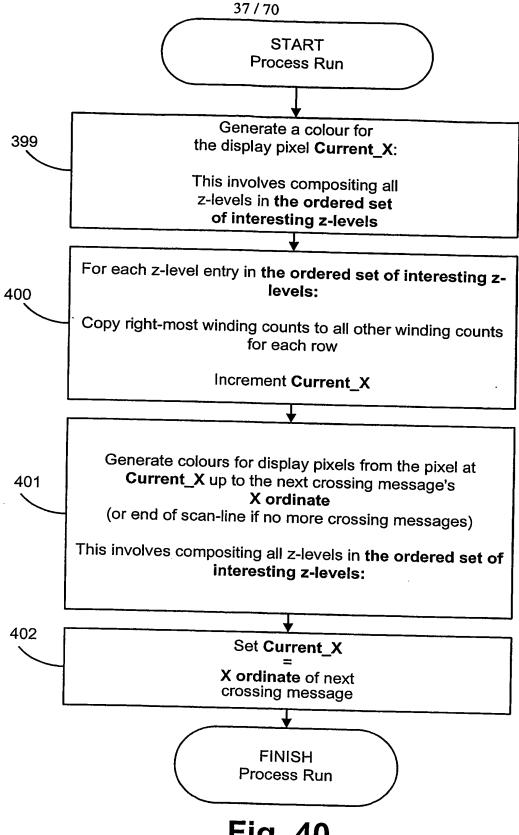
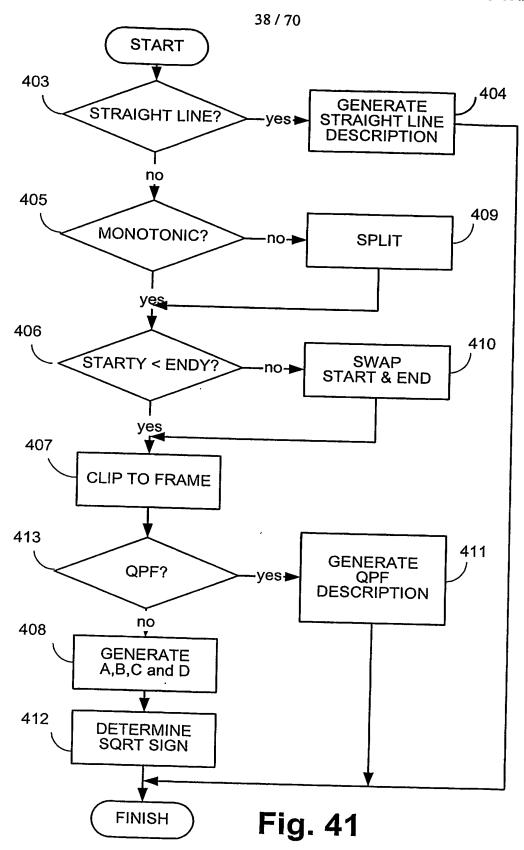
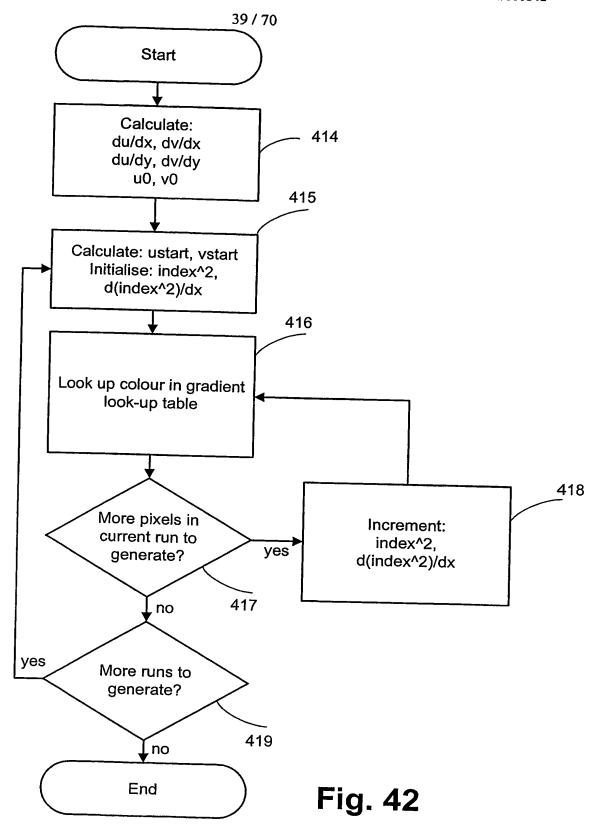
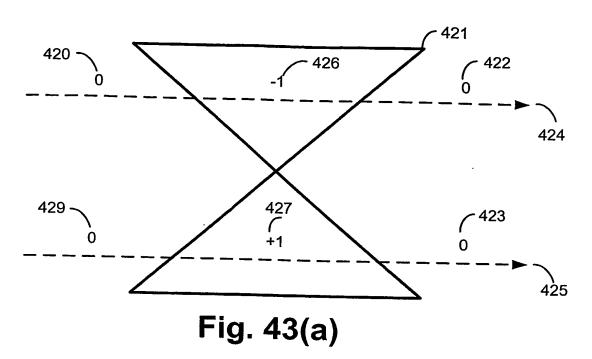
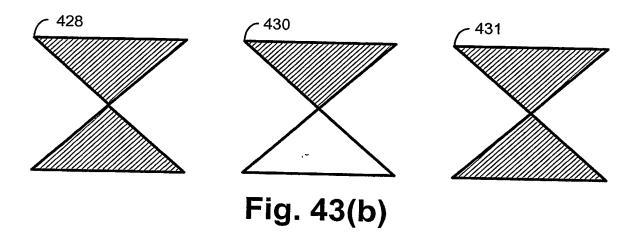


Fig. 40









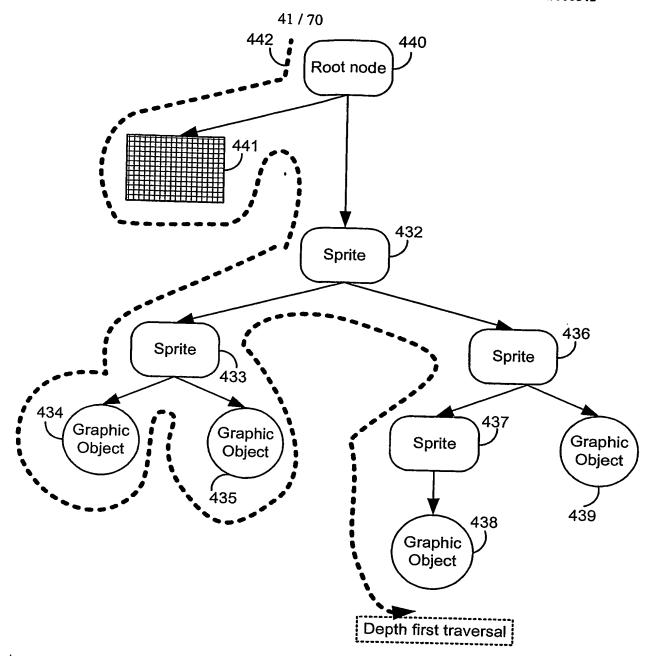
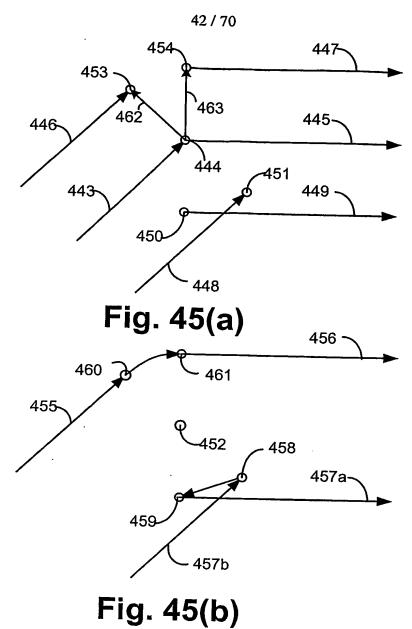
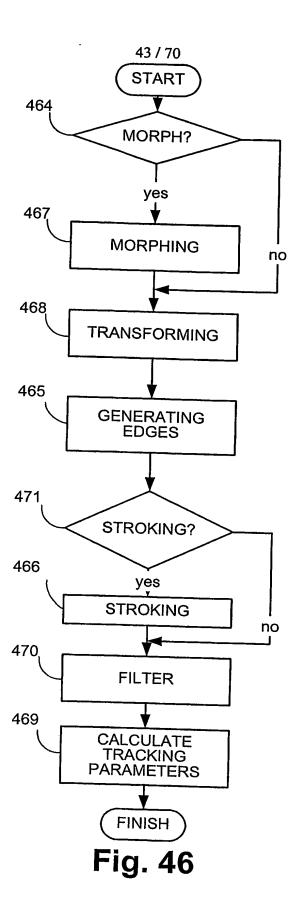
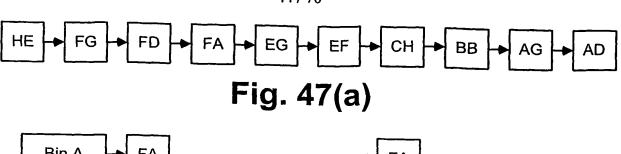


Fig. 44







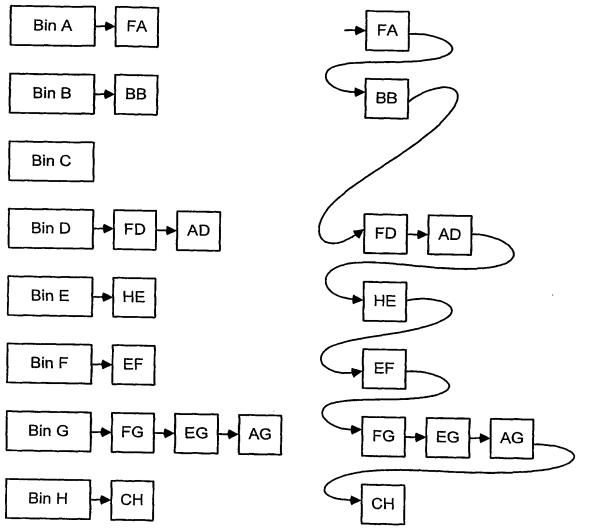


Fig. 47(b)

Fig. 47(c)

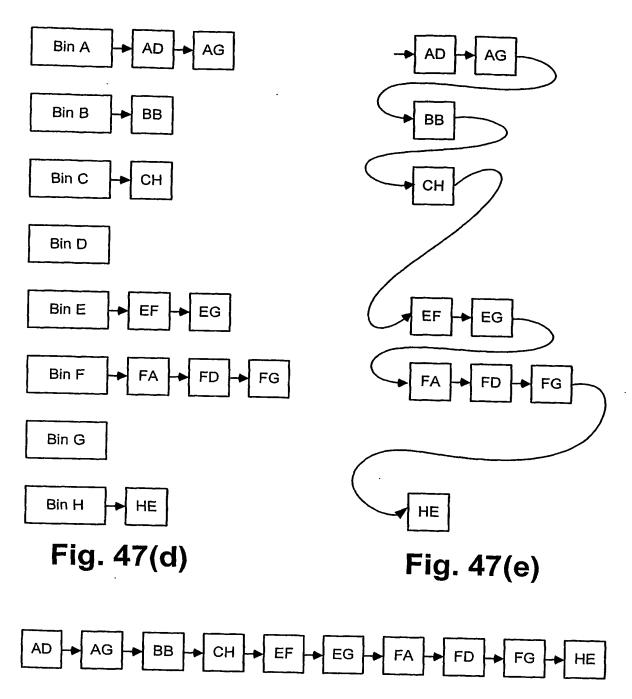
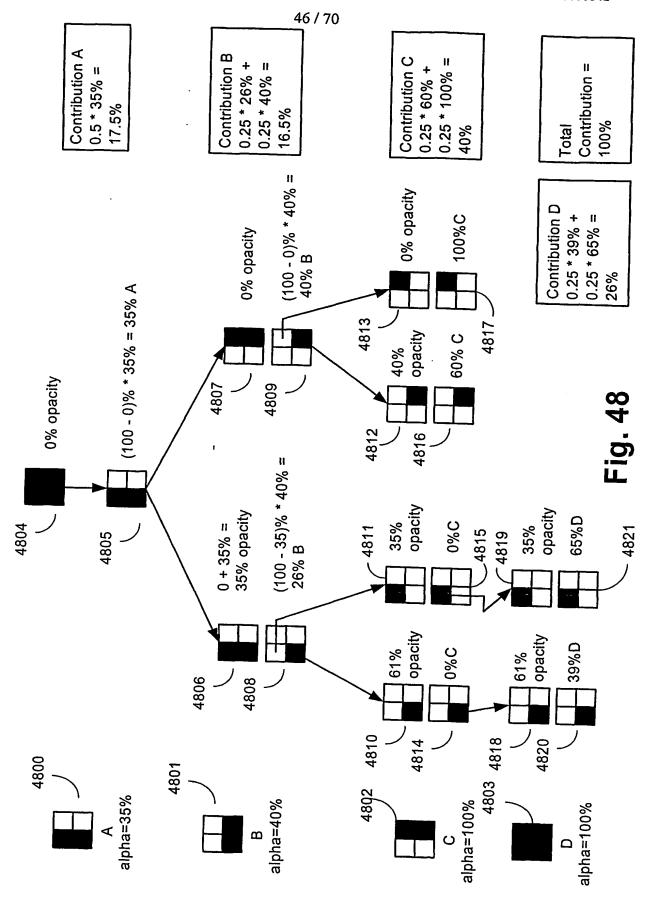
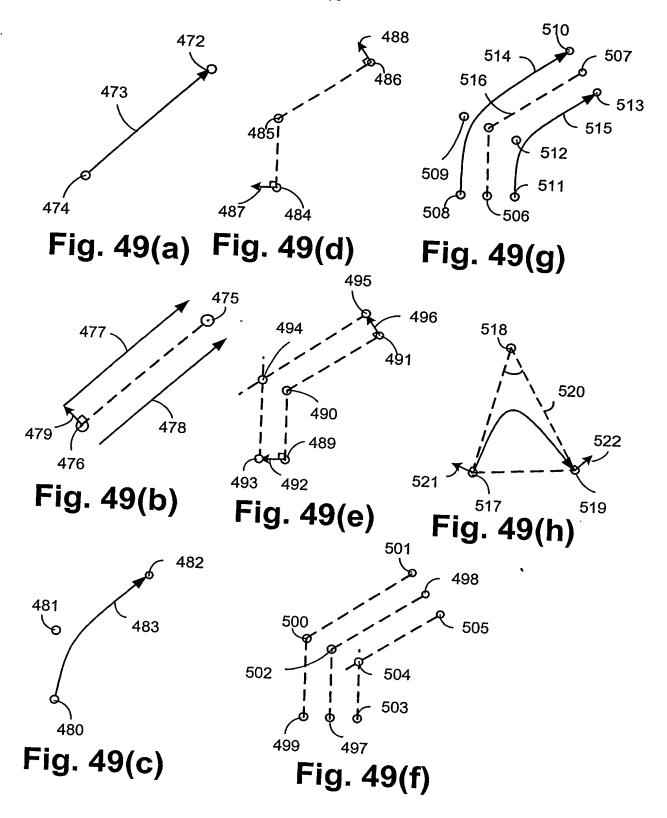
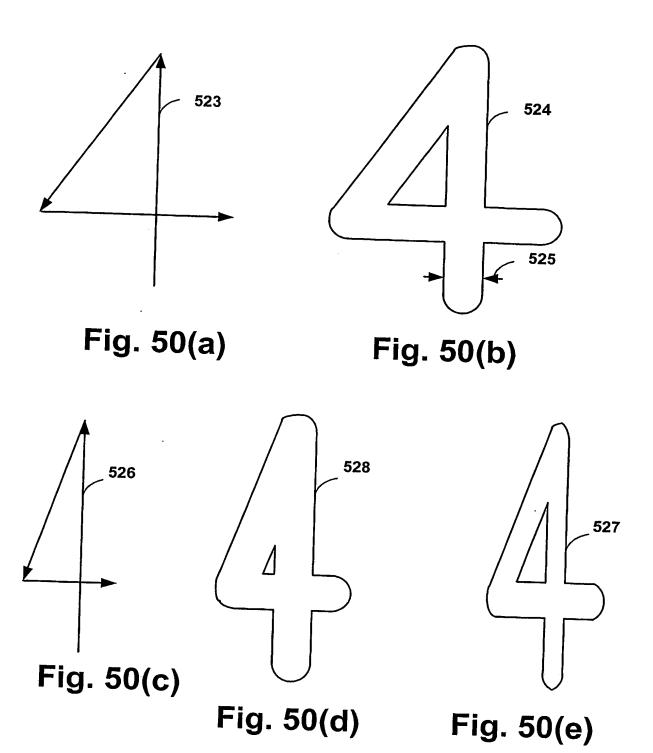
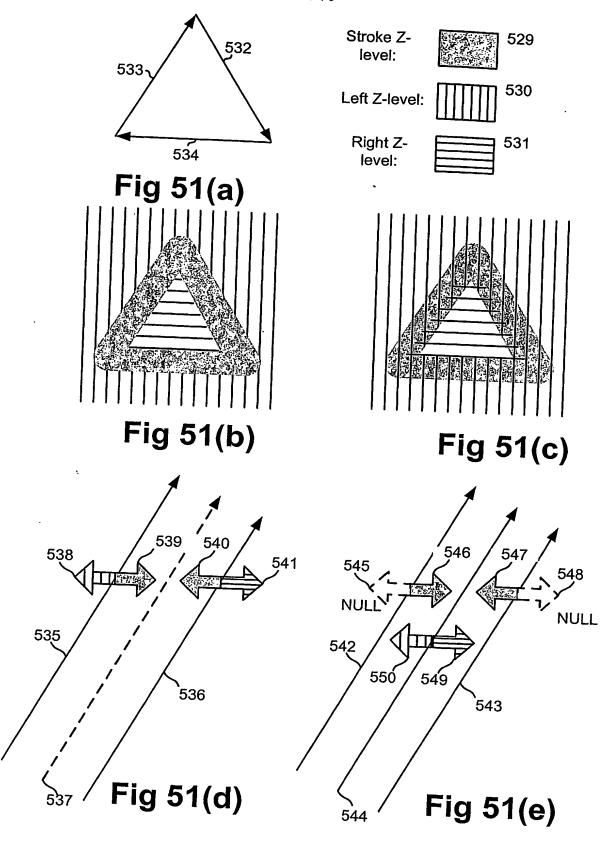


Fig. 47(f)









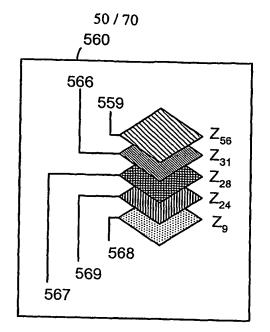


Fig. 52(a)

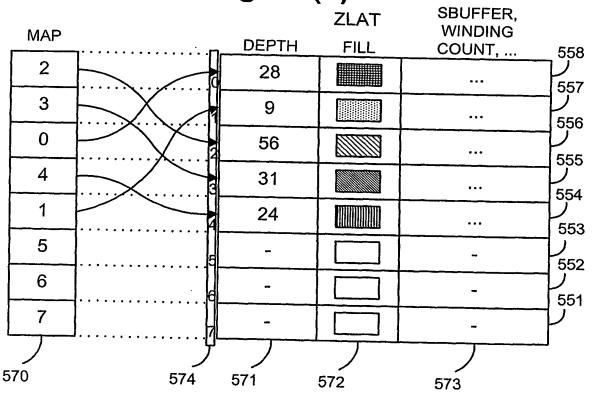
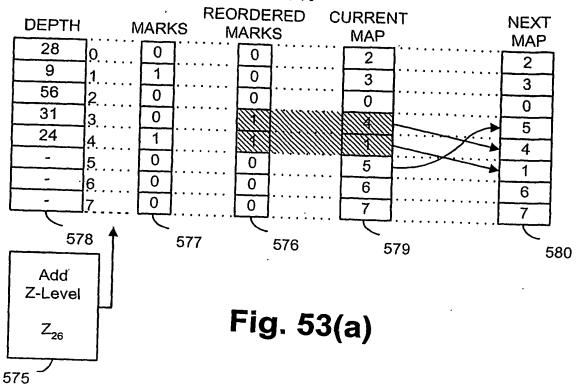
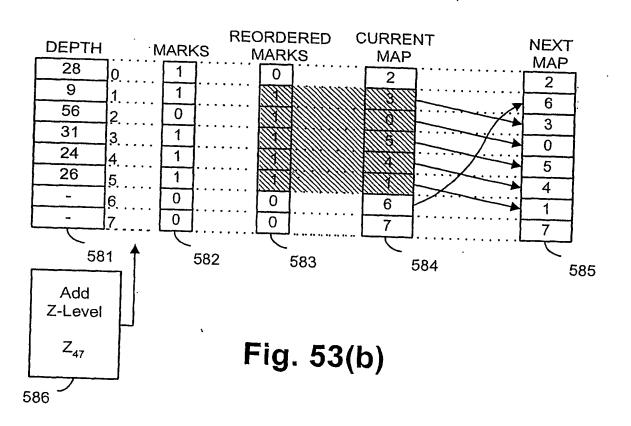
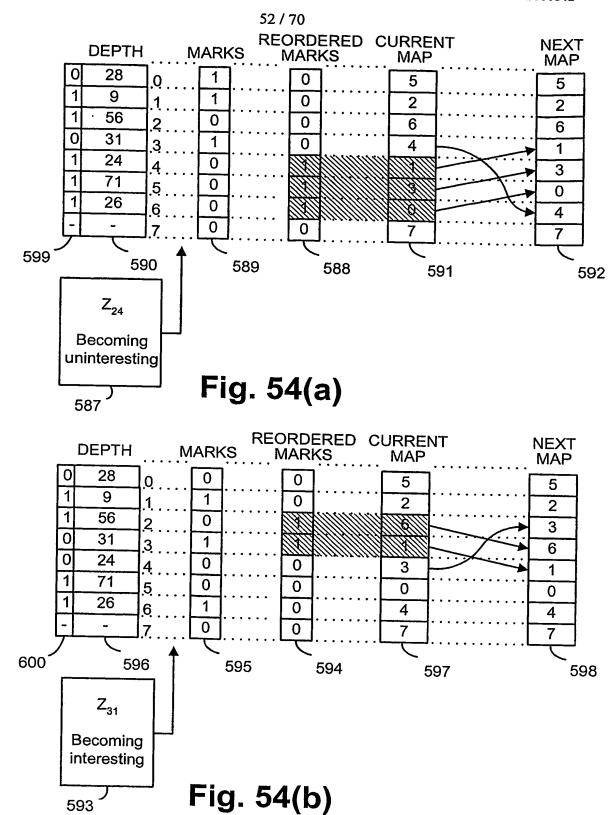


Fig. 52(b)







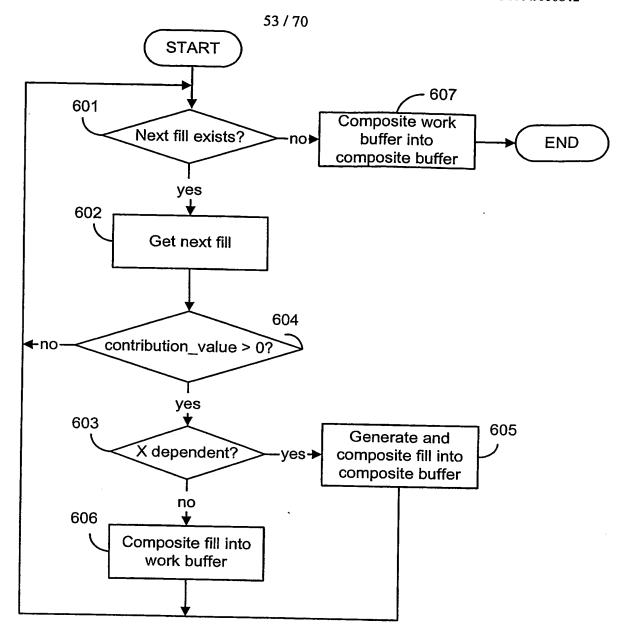
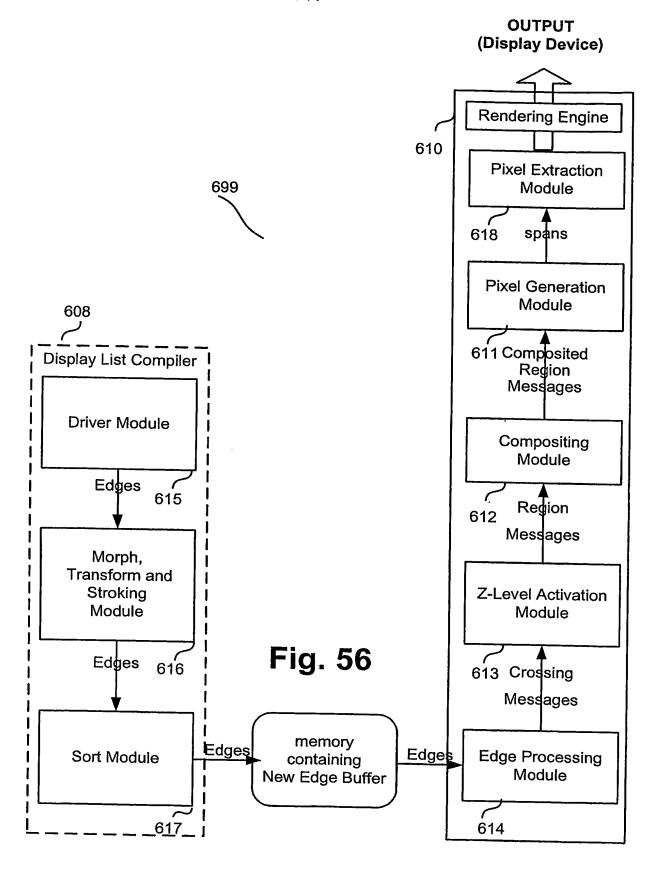


Fig. 55



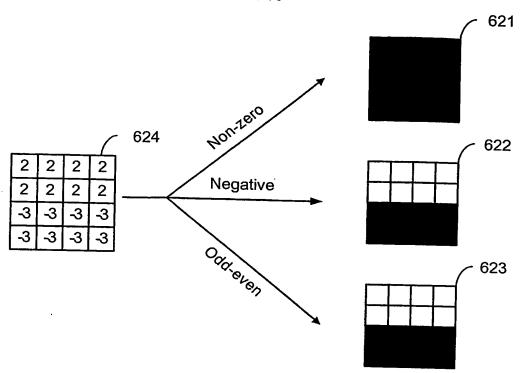
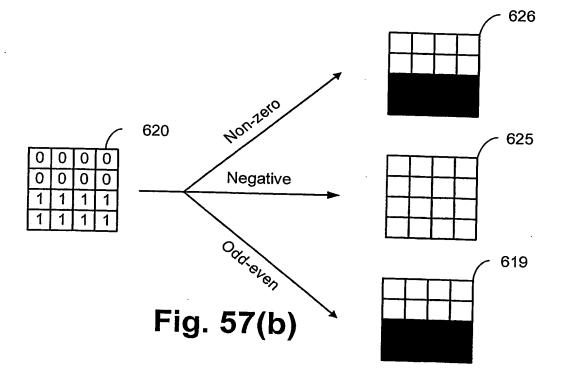


Fig. 57(a)



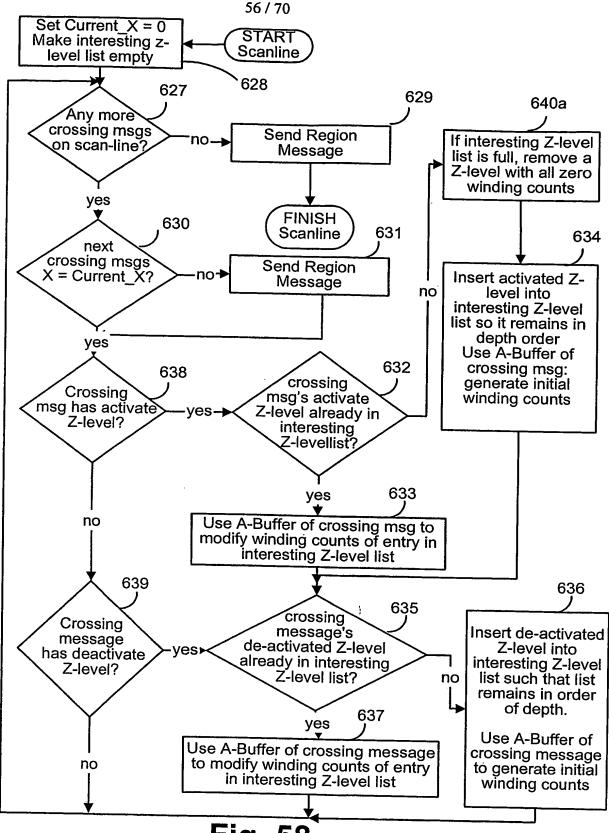
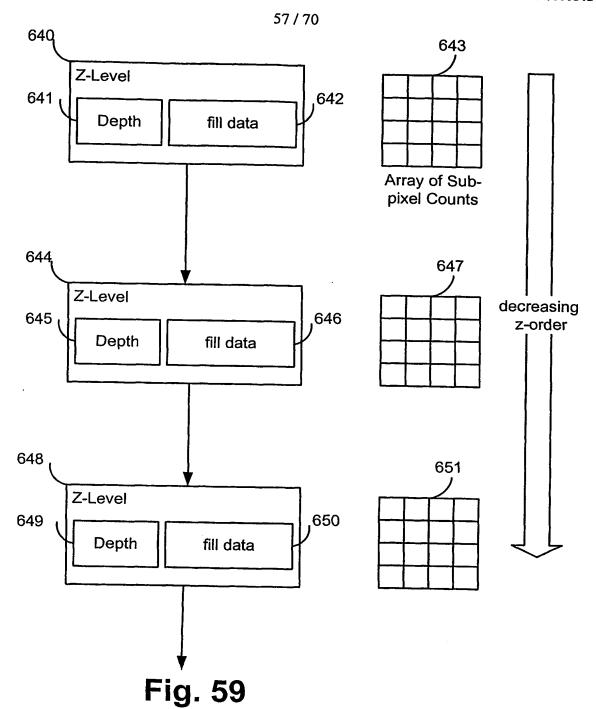


Fig. 58



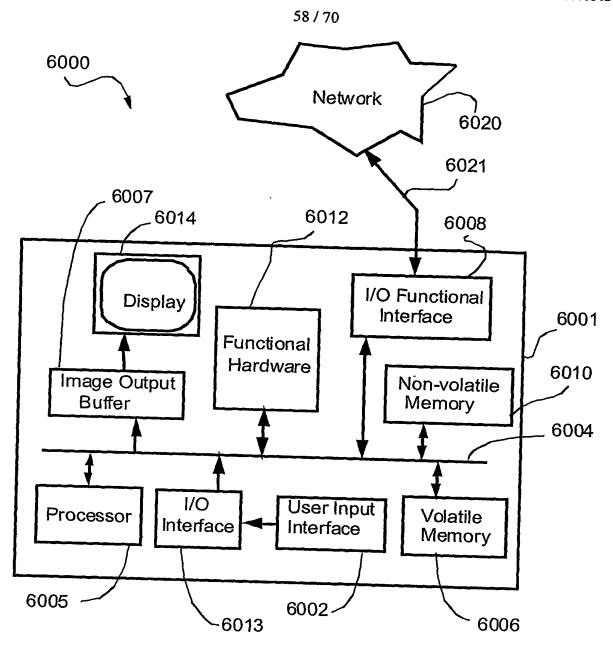


Fig. 60

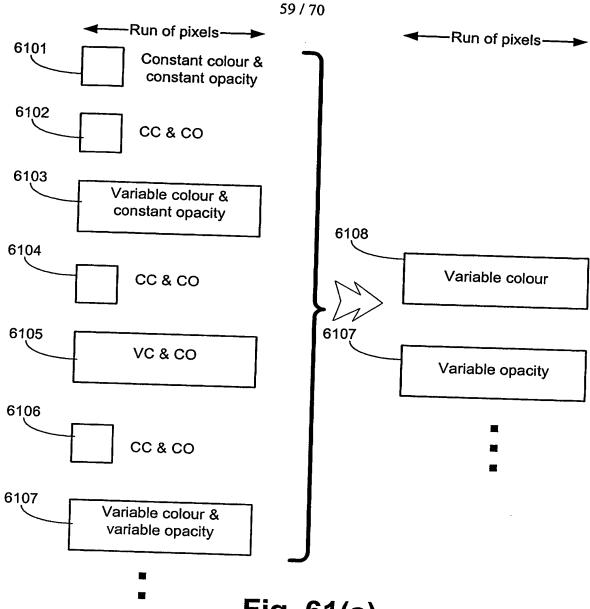


Fig. 61(a)

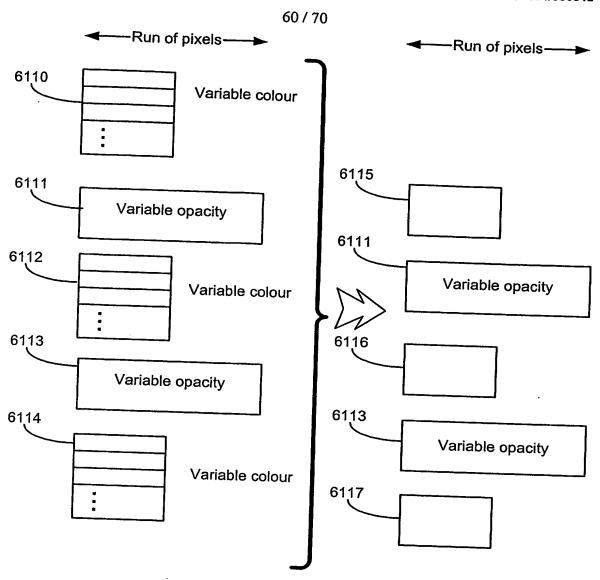


Fig. 61(b)

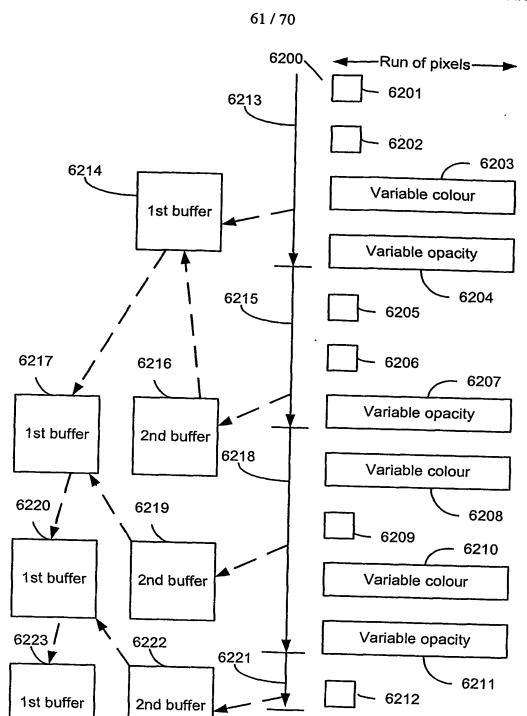


Fig. 62

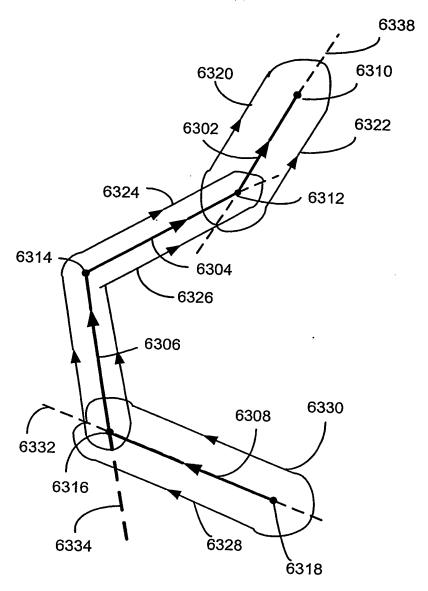


Fig. 63

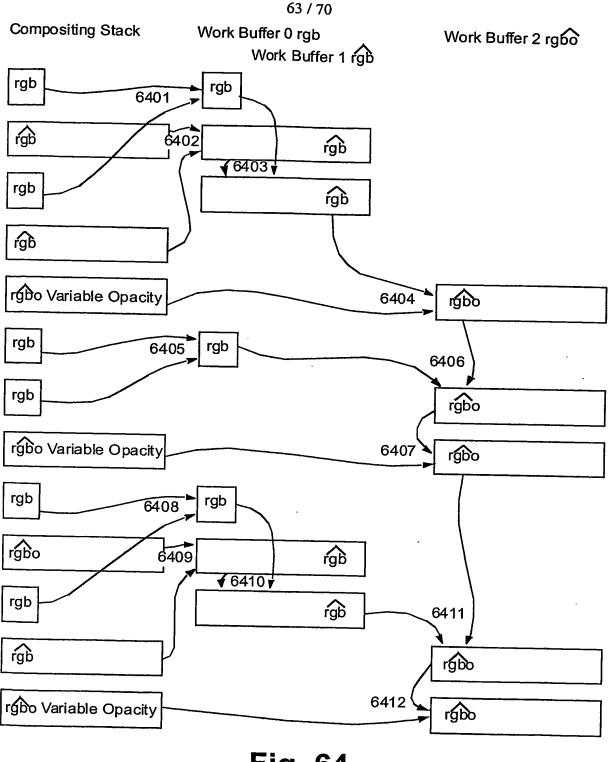
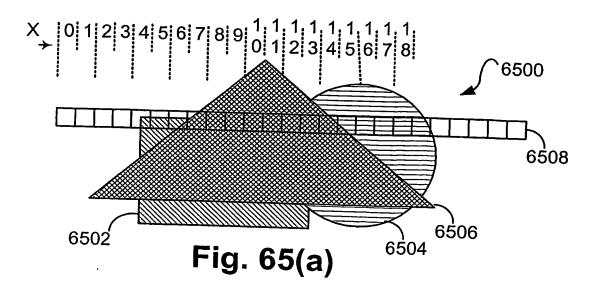
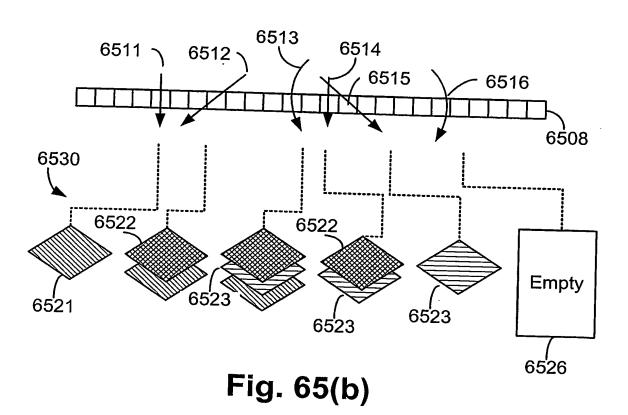


Fig. 64





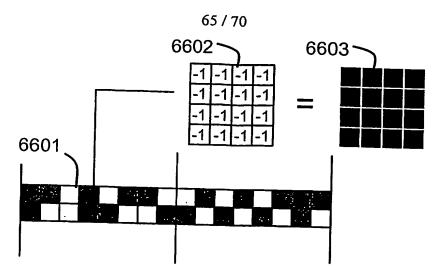
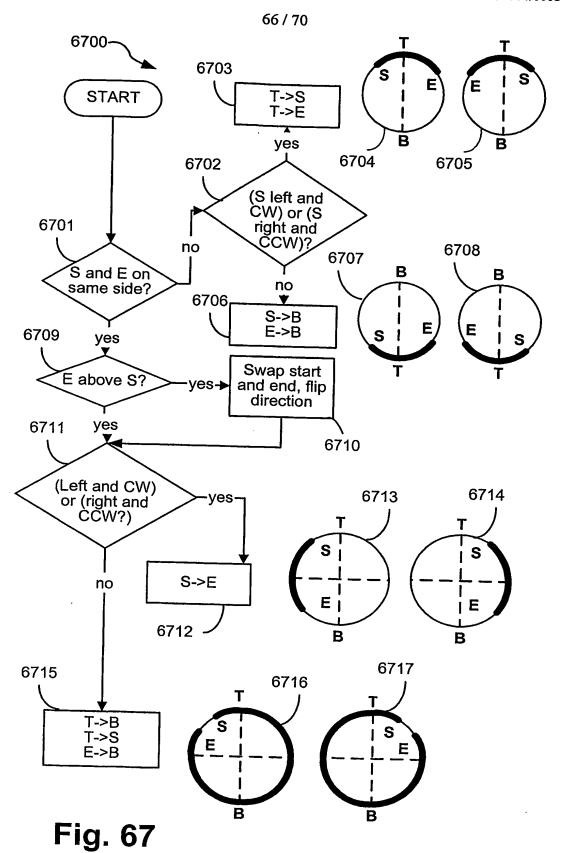
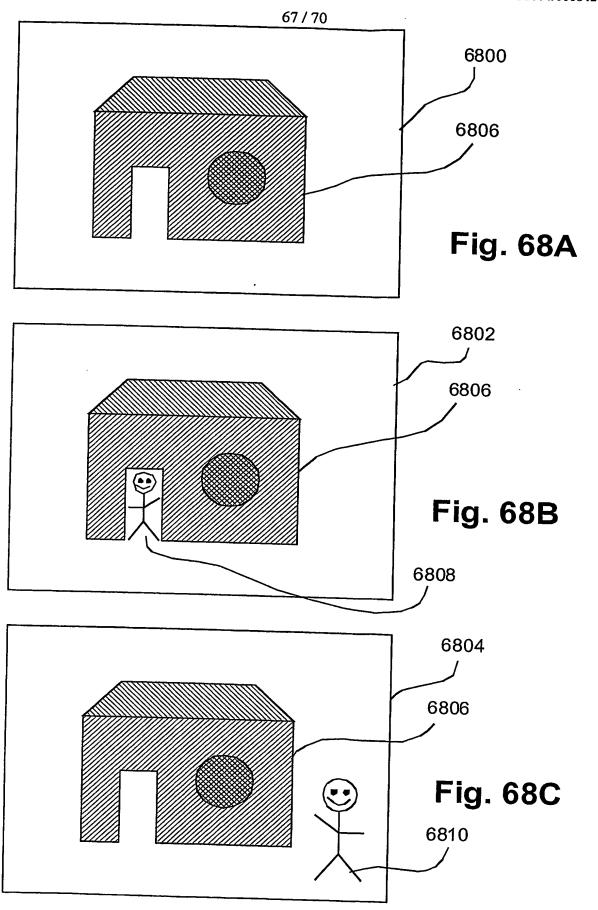
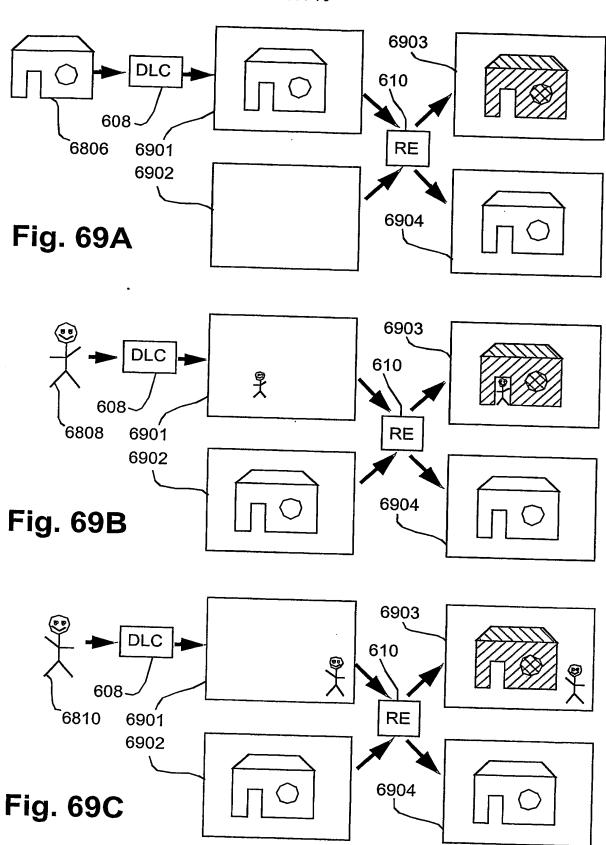


Fig. 66







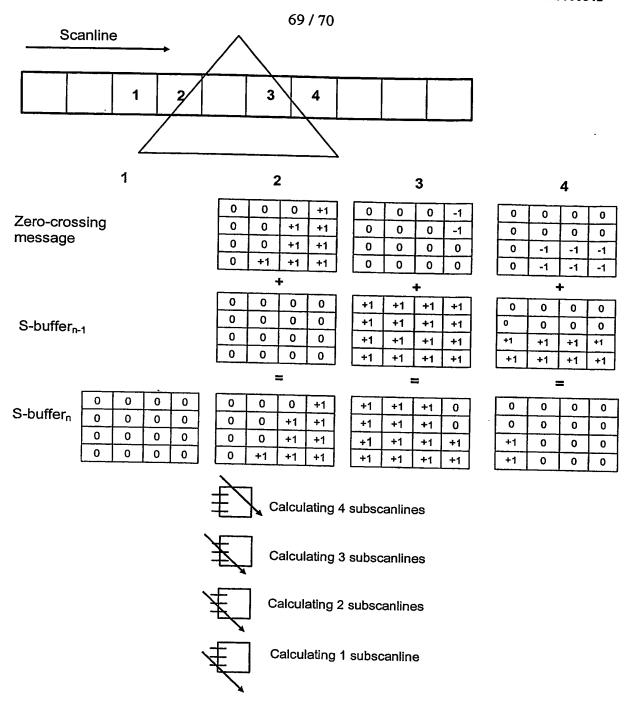


Fig. 70

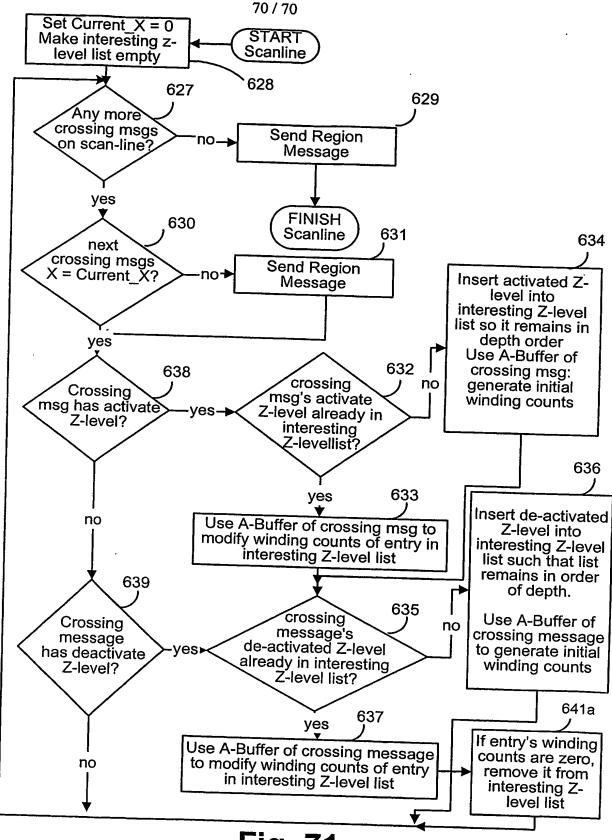


Fig. 71